

CORRECTED VERSION

(19) World Intellectual Property Organization International Bureau



(43) International Publication Date
7 April 2005 (07.04.2005)

PCT

(10) International Publication Number
WO 2005/031968 A1

(51) International Patent Classification⁷:

H03F 1/33

(74) Agent: PAEK, Keon-Soo; 3F, Juhan Bldg., 834-45, Yoksam-Dong, Kangnam-Gu, Seoul 135-914 (KR).

(21) International Application Number:

PCT/KR2004/002465

(81) Designated States (unless otherwise indicated, for every kind of national protection available): AE, AG, AL, AM, AT, AU, AZ, BA, BB, BG, BR, BW, BY, BZ, CA, CH, CN, CO, CR, CU, CZ, DE, DK, DM, DZ, EC, EE, EG, ES, FI, GB, GD, GE, GH, GM, HR, HU, ID, IL, IN, IS, JP, KE, KG, KP, KZ, LC, LK, LR, LS, LT, LU, LV, MA, MD, MG, MK, MN, MW, MX, MZ, NA, NI, NO, NZ, OM, PG, PH, PL, PT, RO, RU, SC, SD, SE, SG, SK, SL, SY, TJ, TM, TN, TR, TT, TZ, UA, UG, US, UZ, VC, VN, YU, ZA, ZM, ZW.

(22) International Filing Date:

24 September 2004 (24.09.2004)

(25) Filing Language:

English

(26) Publication Language:

English

(30) Priority Data:

10-2003-0067740

30 September 2003 (30.09.2003) KR

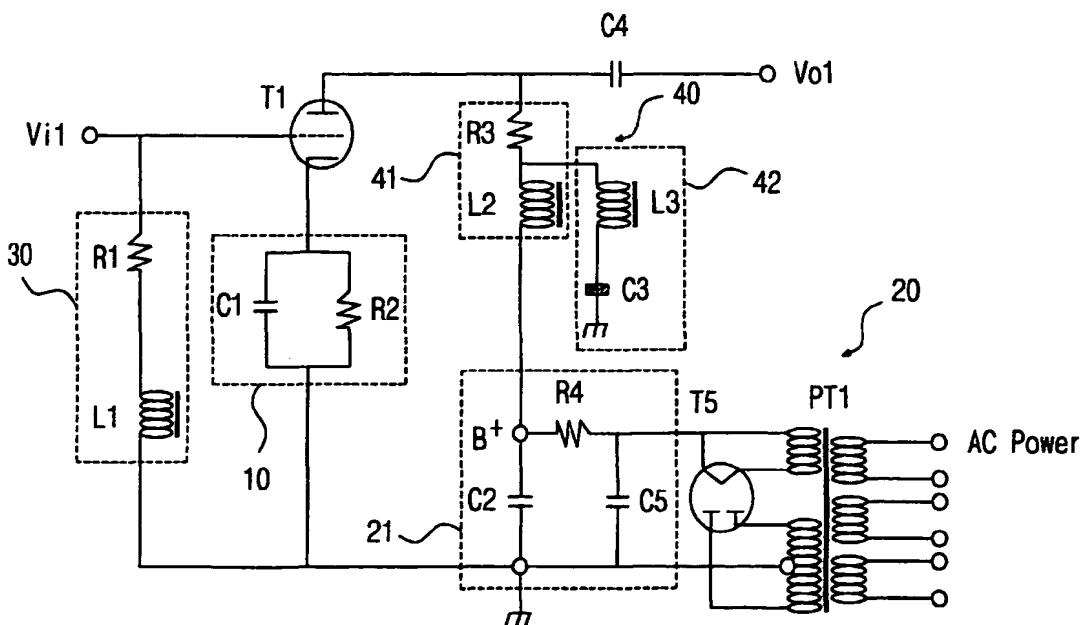
(84) Designated States (unless otherwise indicated, for every kind of regional protection available): ARIPO (BW, GH, GM, KE, LS, MW, MZ, NA, SD, SL, SZ, TZ, UG, ZM, ZW), Eurasian (AM, AZ, BY, KG, KZ, MD, RU, TJ, TM), European (AT, BE, BG, CH, CY, CZ, DE, DK, EE, ES, FI, FR, GB, GR, HU, IE, IT, LU, MC, NL, PL, PT, RO, SE, SI, SK, TR), OAPI (BF, BJ, CF, CG, CI, CM, GA, GN, GQ, GW, ML, MR, NE, SN, TD, TG).

(71) Applicants and

(72) Inventors: LEE, Jong-Ryul [KR/KR]; 107-13, Yeokgok 1(il)-Dong, Wonmi-Gu, Bucheon-Si, Gyeonggi-Do 420-835 (KR). LEE, Jong-Seok [KR/KR]; 220-10, Chunui-Dong, Wonmi-Gu, Bucheon-Si, Gyeonggi-Do 420-857 (KR).

[Continued on next page]

(54) Title: INPUT/OUTPUT SIGNALS PRESERVER CIRCUIT OF AMPLIFICATION CIRCUITS



WO 2005/031968 A1

(57) Abstract: The present invention relates to an input and output signal preservation circuit of an amplification circuit capable of preventing an attenuation of an input signal and an output signal of an amplification circuit in such a manner that an AC input signal is amplified using an amplification device such as a vacuum tube or a transistor in a preamplifier.

**Published:**

- with international search report*
- with amended claims*

Date of publication of the amended claims: 16 June 2005**(48) Date of publication of this corrected version:**

28 July 2005

(15) Information about Correction:

see PCT Gazette No. 30/2005 of 28 July 2005, Section II

For two-letter codes and other abbreviations, refer to the "Guidance Notes on Codes and Abbreviations" appearing at the beginning of each regular issue of the PCT Gazette.